



Programmable processing engine for efficiently processing transient data

Di Kast, PRAKASH, ML, Whigham ... - US Patent 6,513,168, 2003 - Google Patents  
 ... Page 6, US Patent Jan, 28,2003 Sheet 4 of 10 US 6,513,168 Bi LU fy- rv ^~ LU CD CD LU 01  
 CO o Q LU -o Q LU eg CO Q, U, a;— LO CM ex, LU O o 1 OOCCLCCOO - LU 00 ^~ ^~  
 XSCD — LU CO os < Q LO -\*\* CO o fr o CD OO CO UJ < (V u,co Q, Page 7. ...

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**Apparatus and method for using information in one direction of a bi-directional flow in a network to alter characteristics of the return direction flow**

RD Manev, Bt Deenani, ... - US Patent App. 10/166,994, 2002 - Google Patents  
 ... Patent Application Publication Dec, 11,2003 Sheet 7 of 8 US 2003/0227942 AI FIG. 7 POLICY  
 S COUNTER MAPPINGS 536 SIGNATURE ALGORITHM GENERATOR QOS COMPILER HPP  
 COMPILER CE SCRIPT COMPILER n^P IMAGE BUILDER CONTROL I DATA ...  
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**Packet processing system including a policy engine having a classification unit**

CE, Narayan, K, Egan, N, MacAvoy, P, Shankar, ... - US Patent ..., 2000 - Google Patents  
 ... 5, 2000 Sheet 12 of 19 1110 V DMA Ram Base (as defined in the Ring Array diagram) Invalid  
 1112 1102 DMA RESVEC 1110 1102 memory implemented ... 19 RA2 RESVEC 2811 1208 DIN1 WI WO  
 W2 1 1262 BIT-ALU RES-GANG 1240 1254 MEM ADDR 1214^~1 Ifx,-B m rv y^~\_LUJ ...  
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